PBR VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE
(AUTONOMOUS)
(Affiliated to J.N.T.U.A, Approved by AICTE and Accredited by NAAC)
KAVALI – 524201, S.P.S.R Nellore Dist., A.P. India. Ph: 08626-243930

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

PROJECT LABORATORY

We understand the project study as the development of concepts and illustration of possibilities for Laboratory Study. The results of a laboratory project study can form the basis of further decisions, or can be part of the following implementation.

The difference between a laboratory project study and our laboratory feasibility study is, that the project study shows general possibilities without concretely checking the feasibility in consideration of specific conditions. The feasibility study on the other hand, checks one or more selective cases in regard to their feasibility under given circumstances.

The project study asks: How can the idea or advisement look in the implementation?
The feasibility study   asks: Is it possible to realize the project under given circumstances?

HARDWARE Facility: CPU**:** INTEL CORE i5 3rd GEN PROCESSOR, M72 M/B, 4GB RAM, 500GB HDD, KEYBOARD .MOUSE, IBOLL CABINET, MONITOR: 18.5" LED, UPS

SOFTWARE Facility: TURBO C, TURBO C++, WINDOWS OS, MS OFFICE, JDK 1.6, PYTHON,PHP, J2ME TOOL KIT, APACHE TOMCAT 7.0

LAB PHOTOS: